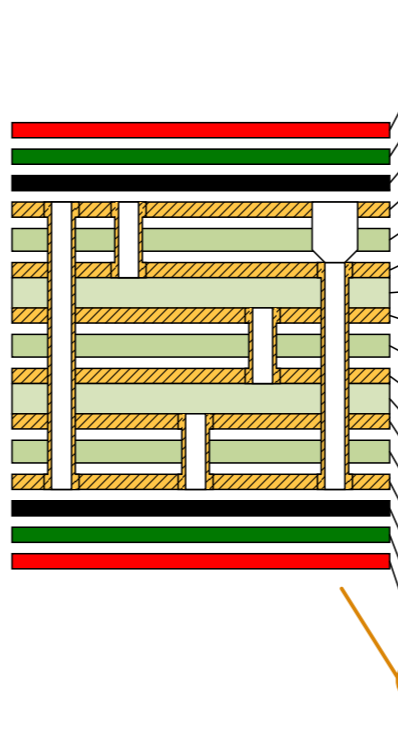


# The Main Advantages when using Draftsman to Create an Order Form

## General description of the PCB

<b>Name of the organization</b>	
Name of the PCB	Kame_FMU.PcbDoc
Number of boards	
<b>Board dimensions</b>	
board length	31.00mm
board width	31.00mm
Number of layers	see Stackup
Board thickness	see Stackup
<b>Holes</b>	
Total number of holes	392
Slot	0
Square Hole	0
Surface Finish	see Stackup
Solder Mask	see Stackup
Solder Mask color	see Stackup
Legend	see Stackup
Minimum hole/pad diameter	see PCB Rules RoutingVias
Minimum clearance/track	see PCBRules Clearance and Width
Milled cutouts (yes/no)	
Contour processing (milling/V-cut)	
Electrical testing (required/not required)	
Impedance control	see Impedance Table

## Layer Stack Legend



Material	Layer	Thickness	Dielectric Material	Type	Df	Dk
	Top Overlay			Legend		
Surface Material	Top Solder	0.01mm	Solder Resist	Solder Mask		3,5
PbSn	Top Surface Finish	0.02mm		Surface Finish		
Copper	Top	0.02mm		Signal		
Prepreg		0.15mm	PP-006	Dielectric	0,02	4,1
CF-004	GND2	0.02mm		Signal		
Core		0.51mm	Core-009	Dielectric	0,02	4,5
CF-004	L3	0.02mm		Signal		
Prepreg		0.15mm	PP-006	Dielectric	0,02	4,1
CF-004	L4	0.02mm		Signal		
Core		0.51mm	Core-009	Dielectric	0,02	4,5
CF-004	GND5	0.02mm		Signal		
Prepreg		0.15mm	PP-006	Dielectric	0,02	4,1
Copper	Bottom	0.02mm		Signal		
PbSn	Bottom Surface Finish	0.02mm		Surface Finish		
Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask		3,5
	Bottom Overlay			Legend		

Total thickness: 1.64mm

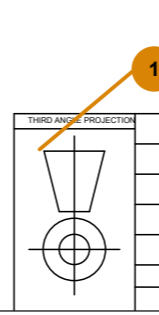
- 1 The order form is a template that can be applied as a Draftsman document template, and then it will be filled with the design data automatically.
- 2 The table is filled with the PCB data automatically.
- 3 Detailed drill table.
- 4 Table with description of transmission lines.
- 5 Layer stackup of the PCB with all parameters.

## Drill Table

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via / Pad	Pad Shape	Template (Mixed)	Hole Tolerance
▽	386	0.20mm	Plated	Round	Top - Bottom	Via			-0.13mm
□	4	0.60mm	Non-Plated	Round	Top - Bottom	Pad	Rounded	c-100hn60(Tol5-5)	+/-0.05mm
☆	2	2.50mm	Plated	Round	Top - Bottom	Pad	Rounded	c500h250(Tol5-18)	+0.05mm/-0.18mm
392 Total									

## Impedance Table

Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Wide Trace Width	Narrow Trace Width	Reference layers
Coated Microstrip	50	50.02	Top	0.28mm	0.28mm	GND2
Offset Stripline	50	49.99	GND2	0.19mm	0.19mm	Top,L3
Offset Stripline	50	49.99	L3	0.19mm	0.19mm	GND2,L4
Offset Stripline	50	49.99	L4	0.19mm	0.19mm	L3,GND5
Offset Stripline	50	49.99	GND5	0.19mm	0.19mm	L4,Bottom
Coated Microstrip	50	50.02	Bottom	0.28mm	0.28mm	GND5



PART NO: =PCB_PART_NUMBER	APPROVALS	DATE	<b>Altium</b> =Address1 =Address2 =Address3 =Address4
ENGINEER: =PCB_ENGINEER	=PCB_ENGINEER		
DESIGNER: =PCB_DESIGNER	=PCB_DESIGNER		
CHECKER: =PCB_CHECKER	=PCB_CHECKER		
Reference Documents			DESIGN ITEM: SRC-Kame_FMU
BOM DOC: =DOC_NO_BOM	TITLE: =PCB_TITLE_1 =PCB_TITLE_2		DESIGN ITEM REVISION: .ItemRevision
ASSY DOC: =DOC_NO_FAB_DWG	SIZE: =CAGE_CODE	DWG NO:	REV:
SCH DOC: =DOC_NO_SCH_DWG	A3 =CAGE_CODE		
PCB DOC: =PCB_DWG_NO	SCALE:	FILE NAME: Blank.PCBdWf	SHEET: 1 OF 1